

----- PIN Declarations -----

PIN 1	CLK1		; INPUT not used
PIN 2	TIMER	COMBINATORIAL	; INPUT TIMER interrupt from IOP
PIN 3	Q4	REGISTERED	; OUTPUT captured ACON interrupt
PIN 4	Q3	REGISTERED	; OUTPUT captured TIMER interrupt
PIN 5	ACON_I	COMBINATORIAL	; INPUT ACON interrupt
PIN 6	IOP_I	COMBINATORIAL	; INPUT IOP interrupt
PIN 7	DPB_I	COMBINATORIAL	; INPUT DPB interrupt
PIN 8	IPL2	COMBINATORIAL	; OUTPUT interrupt code - bit 2
PIN 9	IPL1	COMBINATORIAL	; OUTPUT interrupt code - bit 1
PIN 10	IPL0	COMBINATORIAL	; OUTPUT interrupt code - bit 0
PIN 11	VFB_I	COMBINATORIAL	; INPUT VFB interrupt
PIN 12	GND		
PIN 13	CLK2		; INPUT N/C
PIN 14	CS_INT	COMBINATORIAL	; INPUT capture register select
PIN 15	Q5	REGISTERED	; OUTPUT captured IOP interrupt
PIN 16	Q6	REGISTERED	; OUTPUT captured DPB interrupt
PIN 17	Q7	REGISTERED	; OUTPUT captured VFB interrupt
PIN 18	D7	COMBINATORIAL	; INPUT 68000 data bit
PIN 19	D6	COMBINATORIAL	; INPUT 68000 data bit
PIN 20	D5	COMBINATORIAL	; INPUT 68000 data bit
PIN 21	D4	COMBINATORIAL	; INPUT 68000 data bit
PIN 22	D3	COMBINATORIAL	; INPUT 68000 data bit
PIN 23	NO_NMI	COMBINATORIAL	; INPUT NMI disable from IOP
PIN 24	VCC		

----- Boolean Equation Segment -----

EQUATIONS

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Q3 = VCC
Q3.CLKF = TIMER
Q3.RSTF = D3 * /CS_INT

Q4 = VCC
Q4.CLKF = ACON_I
Q4.RSTF = D4 * /CS_INT

Q5 = VCC
Q5.CLKF = IOP_I
Q5.RSTF = D5 * /CS_INT

Q6 = VCC
Q6.CLKF = DPB_I
Q6.RSTF = D6 * /CS_INT

Q7 = VCC
Q7.CLKF = VFB_I * /NO_NMI
Q7.RSTF = D7 * /CS_INT

IPL0 = /((Q3 * /Q4 * /Q6)
        + (Q5 * /Q6)
        + Q7 * /NO_NMI)

IPL1 = /((Q3 * /Q4 * /Q5)
        + Q6
        + Q7 * /NO_NMI)

IPL2 = /(Q4
        + Q5
        + Q6
        + Q7 * /NO_NMI)
    
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